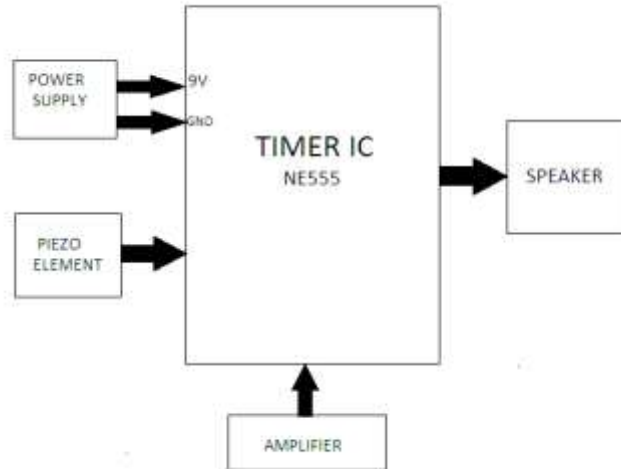




## BLOCK DIAGRAM



*Fig2: Block Diagram of air motion detector*

## WORKING

Figure1 shows the actual circuit diagram of air motion detector alarm, the circuit is build around the piezo sensor element , this piezo element is connected across the connector 2.the transistor BC549(T1),BC 547(T2 Through T5) and BC557 (T6),timer NE55(IC) is in astable mode and other components.

The circuit uses piezo element used in buzzer as sensor, if it sense the strong winds it generate the electric pulses. The signal undergo the multistage amplification by using the transistors, the transistor T1, T2 and T3 are used to amplify the signal as well as they rectified the signal and filter. At the output dc pulse of this transistors T4 and T5 again process of amplification is done. The IC NE555 based on oscillator. The transistor T6 act as switch.

## PIEZO ELEMENT SENSOR

Piezo element is device that used in this project for the purpose of converting the atmospheric pressure such as air into the electric charge, this is also called as transducer. This piezo element has very high dc output impedance, the generated dc voltage is directly proportional to applied input pressure. Mainly two groups of material are used in piezo electric sensor this two are ceramic and crystal material.

## NE 555

We know the working of ic NE555 in this project the ic555 is in astable mode .this ic provide the time delay as an oscillator at the output of this ic is continuous rectangular pulses of specific frequency. Pin8 having given the 9v supply from transformer and pin1 is connected to ground. The speaker is connected at pin no3 of NE555.

These ic is operate at supply voltage 4.5 to 15v. supply current is 3 to 6 mA . normally reset pin4 is held at ground .

## AMPLIFIER

Amplifier perform the main operation in this project, there are two type of transistors used in this project. Amplifier used in project for increasing the power signal generated from piezo element. All transistors in the project are used for amplification purpose , transistor T1,T2,T3 used to rectification and filtering the signal And the signal is undergo the multistage amplifier. transistors are coupled to each other, the transistor T4 and T5 are again amplify the signal.

**TABLE**

Pin	Name	Purpose
1	GND	Ground reference voltage, low level (0 V)
2	TRIG	The OUT pin goes high and a timing interval starts when this input falls below 1/2 of CTRL voltage (which is typically 1/3 V <sub>CC</sub> , CTRL being 2/3 V <sub>CC</sub> by default if CTRL is left open).
3	OUT	This output is driven to approximately 1.7 V below +V <sub>CC</sub> , or to GND.
4	$\overline{\text{RESET}}$	A timing interval may be reset by driving this input to GND, but the timing does not begin again until RESET rises above approximately 0.7 volts. Overrides TRIG which overrides THR.
5	CTRL	Provides "control" access to the internal voltage divider (by default, 2/3 V <sub>CC</sub> ).
6	THR	The timing (OUT high) interval ends when the voltage at THR ("threshold") is greater than that at CTRL (2/3 V <sub>CC</sub> if CTRL is open).
7	DIS	Open collector output which may discharge a capacitor between intervals. In phase with output.
8	V <sub>CC</sub>	Positive supply voltage, which is usually between 3 and 15 V depending on the variation.

*Table1.pins configuration of NE555*

## CONCLUSION

This fragment should obviously state the foremost conclusions of the exploration and give a coherent explanation of their significance and consequence.

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## ACKNOWLEDGEMENTS

A part from the efforts of our, the success of this projects depend largely on the encouragement and guidelines of our guide Mrs.shelke b.s. from E&TC department. The encouragement given by our HOD, Mr. Wakchaure .A.J. & also our project Co-coordinator Mr. Hire .C. P. made our project reach the saturation point.

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